

L Number	Hits	Search Text	DB	Time stamp
-	11	(dual double) adj pump\$3 adj bus\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:52
-	10	((dual double) adj pump\$3 adj bus\$3) and @ad<20000403	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 06:34
-	120	serial same (data adj bit\$1) same latch\$3 same (en enable) same (clk clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 06:36
-	112	(serial same (data adj bit\$1) same latch\$3 same (en enable) same (clk clock)) and @ad<20000403	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 06:42
-	7529	(en Enable) same (clk clock) same (latch (d adj f/f) flipflop f/f)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 06:42
-	1918	((en Enable) same (clk clock) same (latch (d adj f/f) flipflop f/f)) same gate\$1 same circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 06:43
-	1737	((en Enable) same (clk clock) same (latch (d adj f/f) flipflop f/f)) same gate\$1 same circuit) and @ad<20000403	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 06:49
-	1	((en Enable) same (clk clock) same (latch (d adj f/f) flipflop f/f)) same gate\$1 same circuit same (dual double) same pump	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 06:49
-	48	((en Enable) same (clk clock) same (latch (d adj f/f) flipflop f/f)) same gate\$1 same circuit same (dual double)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 06:49
-	39	((en Enable) same (clk clock) same (latch (d adj f/f) flipflop f/f)) same gate\$1 same circuit same (dual double)) and @ad<20000403	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:17
-	608	(signal data) same (serial serializ\$3 serialization) same circuit same gate\$1 same (mux multiplexer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:08
-	24	((signal data) same (serial serializ\$3 serialization) same circuit same gate\$1 same (mux multiplexer)) same (dual pump\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:16
-	0	((signal data) same (serial serializ\$3 serialization) same circuit same gate\$1 same (mux multiplexer)) same (seriam same parallel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:16
-	288	((signal data) same (serial serializ\$3 serialization) same circuit same gate\$1 same (mux multiplexer)) same (serial same parallel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:16

-	233	((signal data) same (serial serializ\$3 serialization) same circuit same gate\$1 same (mux multiplexer)) same (serial with parallel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:22
-	224	((((signal data) same (serial serializ\$3 serialization) same circuit same gate\$1 same (mux multiplexer)) same (serial with parallel)) and @ad<20000403	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:21
-	1031	latch same clock same reset same enable same gate\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:22
-	24	(latch same clock same reset same enable same gate\$1) same (serial with parallel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:23
-	732	clock same (enable with latch\$3) same "AND gate"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:24
-	3	clock with (enable with latch\$3) with "AND gate" with serial same bit\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:24
-	193	clock with (enable with latch\$3) with "AND gate"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:28
-	1372	(dual double) adj pump\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:53
-	9	((dual double) adj pump\$3) same serial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 08:01
-	106	((dual double) adj pump\$3) and gate\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 07:53
-	712	((dual double) adj pump\$3) and (latch\$3 gate\$1 multiplex\$3 clock clk enable en signal data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 08:02
-	19	((dual double) adj pump\$3) and ((latch\$3 gate\$1 multiplex\$3 clock clk enable en signal data ) same serial)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 08:08
-	6	clock adj gating adj technique	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 08:14
-	565	clock adj gating	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/14 08:14

-	19	(clock adj gating) same serial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/14 10:02
-	67	clk same en same (latch f/f flip/flop) same (time interleave)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/14 10:45